

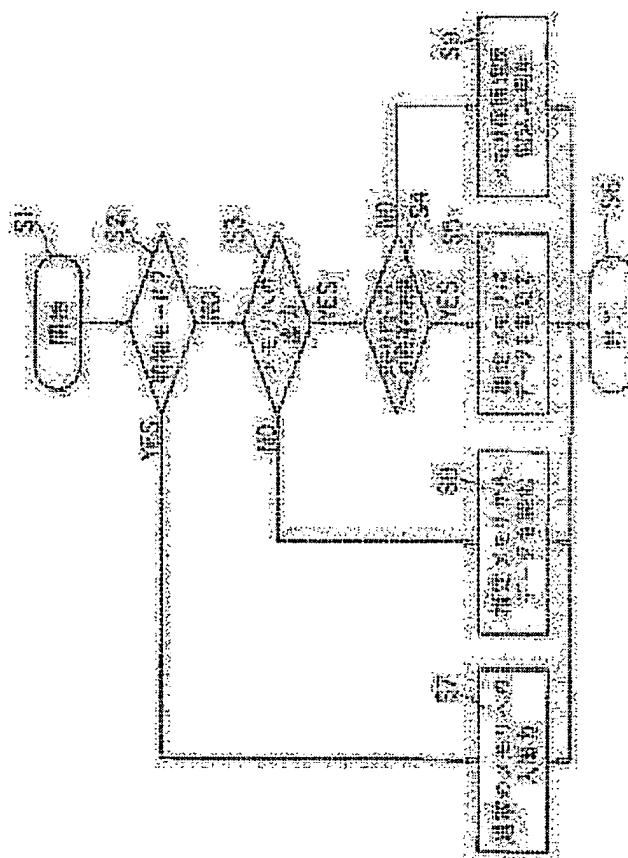
**No title available****Publication number:** JP5334195 (A)**Publication date:** 1993-12-17**Inventor(s):** IWASAKI MASAOKI**Applicant(s):** TOKYO SHIBAURA ELECTRIC CO**Classification:**- international: **G06F11/00; G06F12/14; G06F11/00; G06F12/14; (IPC1-7): G06F12/14; G06F11/00**

- European:

**Application number:** JP19920142873 19920603**Priority number(s):** JP19920142873 19920603**Abstract of JP 5334195 (A)**

**PURPOSE:** To prevent the illegal rewriting by providing a memory protection means decoding the program mode and executing the I/O of data by means of only a prescribed program mode.

**CONSTITUTION:** When the data input and output to RAM is started, the presence or absence of the privileged mode is discriminated S2. When discriminated as 'NO', the data output to the RAM is discriminated S3. If discriminated as 'YES', whether or not the memory address is permitted, is discriminated S4 and the data is written in a specified memory S5 discriminated as 'YES'. When 'YES' is discriminated in the S2, the input and output to the normal RAM is executed S7. In the S3, if the data output to the RAM is judged to be 'NO', the data reading is discriminated, permitting operation of the data from the designated memory S8. In the S7, when 'NO' is discriminated, an interruption of an illegal memory protection is judged to be generated, writing is inhibited S9.

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